

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1084 5	multiplexer and control and inverter and delay	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 10:42	
2	BRS	L2	4419	1 and (delay same inverter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 10:43	
3	BRS	L4	2020	2 and (delay near4 inverter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 10:43	
4	BRS	L5	19	4 and 326/112-114.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:07	
5	BRS	L6	24	4 and (327/403.ccls. 327/407-408.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:07	
6	BRS	L7	1052	327/403.ccls. 327/407-408.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:07	
7	BRS	L8	710	326/112-114.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:07	
8	BRS	L10	269	8 and delay and inverter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:08	
9	BRS	L9	190	7 and delay and inverter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/03 11:19	